AMENDMENTS TO THE SPECIFICATION:

On page 1 after the title, please insert the following:

RELATED APPLICATIONS

The present application is based on International Application No. PCT/JP03/04892, filed April 17, 2003, and claims priority from, Japanese Application Number 2002-115265, filed April 17, 2002, the disclosure of which is hereby incorporated by reference herein in its entirety.

Please amend the second complete paragraph on page 47 as follows:

Fig. 12 is a block diagram showing the configuration of a network analyzer 1 according to the second embodiment. The network analyzer 1 comprises a signal source 10, a receiving element 20, a measurement system error factor recording section 30, a circuit parameter measuring section 40, a measurement system error factor acquiring section 50, a terminal 60 for power meter a signal output acquiring section 62, and further a receiving side measurement system error factor ~~recording~~ acquiring section 70. Hereinafter, the same numerals are assigned to the same components in the first embodiment above to omit descriptions thereof.

Please amend the third complete paragraph on page 47 and 48 as follows:

The switch 13 is for selecting whether an input signal output from the signal outputting section 12 is given to the DUT 2 directly or through the receiving element 20. The switch 13 has terminals 13a, 13b and 13c. The terminal 13a, 13b and 13c are connected respectively, to the output terminal 18, the receiving element 20 and the signal outputting section 12. When the terminals 13a and 13c are connected to each other, that is, the switch 13 is turned to the terminal 13a position, the input signal is to be given to the DUT 2 directly. In this case, the signal outputting section 12 outputs a signal with a frequency of f1. When the terminals 13~~a~~b and 13~~b~~c are connected to each other, that is, the switch 13 is turned to the terminal 13b position, the input signal is to be given to the DUT 2 through the receiving element 20. In this case, the signal outputting section 12 outputs a signal with a frequency of f2.

Please amend the second complete paragraph on page 57 as follows:

The third receiving side measurement system error factor acquiring section 78 receives measurement data of the receiver (RR) 26b and the receiver (TR) 26a Edr, Esr, Er1r and Er2r (measurement system error factors acquired by the second receiving side measurement system error factor acquiring section 76), and measurement data of the receiver (TS) 16b, when the signal source 10 is connected to the receiving element 20, and then acquires Etr and ELr. A method for acquiring Etr and ELr is the same as the third measurement system error factor acquiring section 58 in the ~~second~~ first embodiment.

Please amend the first complete paragraph on page 61 as follows:

Finally, the receiving element 20 is connected to the signal source 10. The signal outputting section 12 outputs an input signal. The receiver (RR) 26b measures the input signal. The input signal is received by the signal source 10 through the input terminal 28 and the output terminal 18. The receiver (TS) 16b measures the signal. Also, the receiver (TR) 26a measures a reflected signal from the signal source 10. Then, the third receiving side measurement system error factor acquiring section ~~58~~ 78 receives the measurement data of the receiver (RR) 26b, Edr, Esr, Er1r and Er2r (measurement system error factors acquired by the second receiving side measurement system error factor acquiring section 76), and. the measurement data of the receiver (TR) 26a and the receiver (TS) 16b, and then acquires Etr and ELr (S127).

Please amend the second complete paragraph on page 61 as follows:

Referring now back to Fig. 15, the DUT 2 is connected to the network analyzer 1 (refer to Fig. ~~1~~12) and the S parameters, etc. (R, S11m, S21m, S12m and S22m) of the DUT 2 are measured (S20).